

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 64-019446
(43)Date of publication of application : 23.01.1989

(51)Int.Cl. G06F 12/06

(21)Application number : 62-174624 (71)Applicant : HITACHI LTD
(22)Date of filing : 15.07.1987 (72)Inventor : HAGINO KEIKO

(54) SEMICONDUCTOR STORAGE DEVICE

(57)Abstract:

PURPOSE: To improve the use efficiency of an internal storage area by enabling access to the divided storage area in accordance with a value preliminarily set by an address converting circuit.

CONSTITUTION: An address conversion table TBL is provided which receives system address signals A0'WA20, whose number of bits is larger than that of address signals A0'WA14' required for designation of the internal storage area, to designate the storage area divided into plural banks AWC in accordance with an arbitrary system address signal. Since the access to the divided storage area is performed in accordance with the preliminarily set value by the address conversion circuit TBL, the internal storage area is efficiently used.

